

X2 CWDM 40 km transceiver | 10G ER Ethernet

Datasheet

X2 Optical Transceiver

Product Features

- 10GBASE-ER/EW Ethernet 14dB X2
- 40 km ER X2 for SMF @ 10Gbps
- 1470nm 1610nm EML+PIN Laser 40 km X2
- 0°C 70°C Temperature Extended/Industrial Available
- 2-Wire Interface Digital Diagnostic Monitoring (SFF-8724)
- Hot-swappable for X2 LC ports
- **OptoSpan 1 year standard warranty**
- Use with Finisar, Avago, JDSU & networks not requiring OEM compatibility
- X2 MSA / IEEE 802.3ae/q/k
- RoHS compliant
- Applications * For OEM Compatibility, use Platinum Series Part# PX2T-10GCXXK040

Description

OptoSpan X2T-10G-K040CXX is a CWDM 10GBASE-ER/EW Ethernet X2 transceiver designed for long distance optical communications up to 40 km with signaling rates up to 10Gbps.

OptoSpan 10Gb CWDM optical transceivers are compatible with many brands such as Finisar, Avago, JDSU and network environments that do not require any special compatibility. For networks that require special OEM compatibility, such as CISCO, BROCADE, JUNIPER, ALCATEL, HP, NORTEL, EMC, QLOGIC and other OEMs, consider OptoSpan Platinum OEM Series transceiver model# PX2T-10GCXXK040.

All OptoSpan long-reach X2 s are ROHS compliant, allow for real-time diagnostic monitoring as per SFF-8472 and designed to meet Multi-Source Agreement (MSA) standards for CWDM transceivers with LC interface.

X2T-10G-K040CXX	Distance: 40 km				Fiber: 1470nm - 1610nm	
	Tx Min dBm	Tx Max dBm	Rx Min dBm	Rx Max dBm	Link Attenuation dB	Power Budget dB
Product Specifications	-1	4	-15	0.5		
Optical Calculation Results			-14.8	-9.8	13.8	14

Optical Budget Calculation for 40 km X2 Optical Transceiver

X2T-10G-K040CXX



- 10 Gigabit Ethernet
- Fibre Channel 8x
- 10GBASE-ER @ 10.31Gbps

Optospan

X2 CWDM 40 km transceiver | 10G ER Ethernet General Specifications

Parameter	Unit	Min.	Тур.	Мах
Ab	solute Maximu	m Ratings		
Maximum Supply Voltage	V	-0.3		4.0
Storage Temperature	°C	-40		85
Case Operating Temperature	°C	-5		+70
Recom	mended Opera	ting Condition		
Supply Voltage	V	3.14	3.3	3.47
Supply Current	mA	310	360	576
Data Rate	Gbps		10.51875	

Electrical Characteristics

Parameter	Unit	Min.	Тур.	Max
	Transmitt	er		
Differential Input Voltage Swing	mVpp	175		2000
Input Differential Impedance	ohm	80	100	120
Transmit Disable Voltage - High	V			
Transmit Disable Voltage - Low	V			
Transmit Fault Voltage - High	V			
Transmit Fault Voltage - Low	V			
	Receive	r		
Differential Output Voltage Swing	mVpp	800		1600
Differential Output Impedance	ohms	80	100	120
LOS Output Voltage - High	V			
LOS Output Voltage - Low	V			



X2 CWDM 40 km transceiver | 10G ER Ethernet

Optical Characteristics

Parameter	Unit	Min.	Тур.	Max
	Transmitt	er		
Output Optical Power	dBm	-1		4
Optical Extinction Ratio	dB	3.5		
Optical Wavelength	nm			
Spectral Width	nm			
Side Mode Suppression Ratio	dB	32		
	Receive	r		
Optical Center Wavelength	nm			
Receiver Sensitivity @ 10.31Gbp	dBm	-15		0.5
LOS DE-Assert	dBm			
LOS Assert	dBm			

Laser Safety

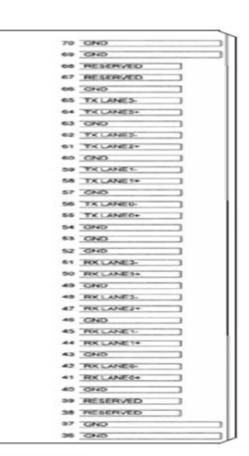
This is a class 1 Laser Product according to IEC 60825-1:1993:+A1:1997+A2:2001. This product complies with 21 CFR 1040.10 and 1040 except for deviations pursuant to Laser Notice No. 50, dated July 26, 2001.

Optospan

Z

X2 CWDM 40 km transceiver | 10G ER Ethernet PIN Layout

1 GND]	II.
2 040		
a GND		
4 5.0V		1
6 3.3V		н
6 3.3V		н
7 APS		
a APS	1	11
e LASI		н
10 REBET		н
11 VENDSPE	ECIFIC	н
12 TX CN/OF	F	н
13 RESERVE	D	L
14 MOD DET	ECT	L
15 VENDER	ECIFIC]	L
16 VENDSPE	ECIFIC	L
17 MDIO		÷.
18 MDC		L
19 PRTAD4		L
20 PRTADS		T
21 PRTAD2	1	T
22 PRTADI		Т
23 PRTADO		L
24 VENDEN	ECHFIC	Т
26 APS SET	1	L
20 RESERVE	D	T
27 APS 5648	56	
20 APS]	ıL.
29 APS		ıĿ.
30 3.3V		Ł
31 3.37		1
32 5.94		
33 7545		
34 GND	1	1
35 GND		1







Optospan

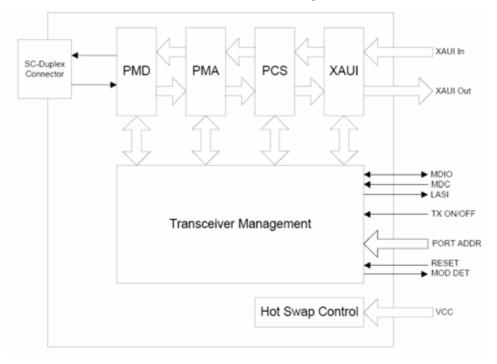
X2 CWDM 40 km transceiver | 10G ER Ethernet

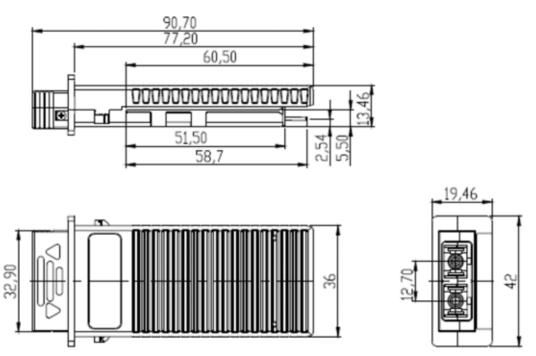
PIN Functions

1 E 2 E 3 E 4 P 5 P 6 P 7 A 8 A 9 L	Name - Description Electrical Ground Electrical Ground Electrical Ground Power Supply of Optical Receiver Frontend Power Supply of Optical Receiver and Transmitter and Control Circuits Power Supply of Optical Receiver and Transmitter and Control Circuits Power Supply of Optical Receiver and Transmitter and Control Circuits Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
2 E 3 E 4 P 5 P 6 P 7 A 8 A 9 L	Electrical Ground Electrical Ground Power Supply of Optical Receiver Frontend Power Supply of Optical Receiver and Transmitter and Control Circuits Power Supply of Optical Receiver and Transmitter and Control Circuits Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
3 E 4 P 5 P 6 P 7 A 8 A 9 L	Electrical Ground Power Supply of Optical Receiver Frontend Power Supply of Optical Receiver and Transmitter and Control Circuits Power Supply of Optical Receiver and Transmitter and Control Circuits Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
4 P 5 P 6 P 7 A 8 A 9 L	Power Supply of Optical Receiver Frontend Power Supply of Optical Receiver and Transmitter and Control Circuits Power Supply of Optical Receiver and Transmitter and Control Circuits Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
5 P 6 P 7 A 8 A 9 L	Power Supply of Optical Receiver and Transmitter and Control Circuits Power Supply of Optical Receiver and Transmitter and Control Circuits Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
6 P 7 A 8 A 9 L	Power Supply of Optical Receiver and Transmitter and Control Circuits Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
7 A 8 A 9 L	Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
8 A 9 L	Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
9 L	Link Alarm Status Interrupt, Iow active, Open Drain Output Supposed to operate Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
	Low active Reset Input Vendor Specific Pin,. for proper operation leave unconnected
10 L	Vendor Specific Pin,. for proper operation leave unconnected
11 V	
12 H	High active Transmitter Enable Input 10kilohms pull-up on Transceiver Logic high =
13 R	Reserved by MSA, internally not connected
14 1	1kilohms to Ground for APS Circuit Environment
15 V	Vendor Specific Pin,. for proper operation leave unconnected
16 V	Vendor Specific Pin,. for proper operation leave unconnected
17 N	Management Data IO
18 N	Management Clock Input
19 P	Port Address Bit 4 (Low = 0), internally pulled up by 18kilohms
20 P	Port Address Bit 3 (Low = 0), internally pulled up by 18kilohms
21 P	Port Address Bit 2 (Low = 0), internally pulled up by 18kilohms
22 P	Port Address Bit 1 (Low = 0), internally pulled up by 18kilohms
23 P	Port Address Bit 0 (Low = 0), internally pulled up by 18kilohms
24 V	Vendor Specific Pin,. for proper operation leave unconnected
25 F	Feedback Input for APS, Input of APS Setting Resistor
26 R	Reserved for Avalanche Photodiode use, internally not connected
27 A	APS Sense Output for APS Control Circuit
28 A	Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices
29 A	Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices
	Power Supply of Optical Receiver and Transmitter and Control Circuits



X2 CWDM 40 km transceiver | 10G ER Ethernet Mechanical Layouts





OptoSpan reserves the right to make changes or to discontinue any optical product or service without any notice. Applications and features described herein are for illustrative purposes only. OptoSpan makes no representation of warranty that such applications or features will be suitable for any specific use or compatibility without further testing or modifications. Not responsible for typographical errors.